

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of	Docket No: Q85096
Masamichi INENAGA, et al.	Allowed: June 5, 2008
Appln. No.: 10/516,693	Group Art Unit: 2886
Confirmation No.: 6877	Examiner: Isiaka O. Akanbi
Filed: December 6, 2004	Issued: October 14, 2008
U.S. Patent No.: 7,436,513	

For: WAFER PRE-ALIGNMENT APPARATUS AND METHOD

**SUBMISSION OF ART**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

For the possible benefit of anyone subsequently evaluating the scope and/or validity of the above-identified patent, it is requested that the document that is listed below (copy enclosed) be placed in the U.S. Patent and Trademark Office's file wrapper of the above-identified U.S. patent:

**JP-A-09-229713**

**JP-A-2002-158275**

The undersigned has not reviewed the teachings of the above-listed document in detail and thus makes no representations concerning the relevancy or materiality of the above-listed document.

This is not an Information Disclosure Statement and no response from the U.S. Patent and Trademark Office is believed to be necessary, nor are any fees believed to be due.

Respectfully submitted,

/Chidambaram.S.Iyer/

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